

	Typ e	Hit s	Search Text	DBs	Time Stamp	C o m m e n t s	E r r o r s
29	BRS 1		(Mambakkam-Sreenath.in. Venkidu-Arockiyaswamy.in. Jones-Larry-Lawson.in.) and @pd>20040210 (361/737.ccls: 710/302.ccls. 710/301.ccls. 710/64.ccls. 710/63.ccls. 710/62.ccls. 235/492.ccls. 235/435.ccls. 235/439.ccls. 235/440.ccls. 235/441.ccls.) and @pd>20040210	USPAT; US-PGPUB; EPO; JPO	2004/05/18 08:33		0
30	BRS 210			USPAT; US-PGPUB	2004/05/18 13:04		0
31	BRS 0		6722572.pn. and parallel	USPAT; US-PGPUB	2004/05/18 10:01		0
32	BRS 0		6722572.pn. and simultaneously	USPAT; US-PGPUB	2004/05/18 10:01		0
33	BRS 0		6722572.pn. and simultaneous	USPAT; US-PGPUB	2004/05/18 10:01		0
34	BRS 0		6722572.pn. and concurrent\$2	USPAT; US-PGPUB	2004/05/18 10:01		0
35	BRS 8		((memory smart ic chip circuit) adj1 card\$1) near5 (adapter reader terminal interface) near5 (plural\$3 many several multiple) near5 (type\$1 format\$1 standard\$1) and @pd>20040210 ((memory smart ic chip circuit) adj1 card\$1) near5 (adapter reader terminal interface) near5 (plural\$3 many several multiple) near5 (type\$1 format\$1 standard\$1) and @pd>20040210	USPAT; US-PGPUB	2004/05/18 10:14		0
36	BRS 1		(scsi (small adj computer adj system adj interface)) near5 (simple adj display adj device) and @pd>20040210	EPO; JPO; DERWENT; IBM_TDB	2004/05/18 10:21		0
37	BRS 0		(scsi (small adj computer adj system adj interface)) near5 (simple adj display adj device) and @pd>20040210	USPAT; US-PGPUB	2004/05/18 10:21		0
38	BRS 10		(scsi (small adj computer adj system adj interface)) near5 (display screen lcd) and @pd>20040210	USPAT; US-PGPUB	2004/05/18 10:22		0

	Typ e	Hit s	Search Text	DBs	Time Stamp	C o m m e n t s	E r r o r s
39	BRS	0	(scsi (small adj computer adj system adj interface)) near5 (display screen lcd) near5 (command\$3 instruction\$1) and @pd>20040210	USPAT; US-PGPUB	2004/05/18 10:26		0
40	BRS	0	((memory smart ic chip circuit) adj1 card\$1) near5 (adapter reader terminal interface) near5 (plural\$3 many several multiple) near5 (type\$1 format\$1 standard\$1)) same parallel and @pd>20040210	USPAT; US-PGPUB	2004/05/18 10:28		0
41	BRS	5	((memory smart ic chip circuit) adj1 card\$1) near5 (adapter reader terminal interface) near5 (plural\$3 many several multiple) near5 (parallel simultaneous\$2 concurrent\$2) near5 (access\$3 read\$3 writ\$3 communicat\$3)	USPAT; US-PGPUB	2004/05/18 10:39		0
42	BRS	1	5679007.pn. and (parallel simultaneous\$2 concurrent\$2)	USPAT; US-PGPUB	2004/05/18 12:42		0
43	BRS	34	((memory smart ic chip circuit) adj1 card\$1) near5 (plural\$3 many several multiple) near5 (parallel simultaneous\$2 concurrent\$2) near5 (access\$3 read\$3 writ\$3 communicat\$3)	USPAT; US-PGPUB	2004/05/18 11:12		0
44	BRS	17	((memory smart ic chip circuit) adj1 card\$1) near5 (plural\$3 many several multiple) near5 (parallel simultaneous\$2 concurrent\$2) near5 (access\$3 read\$3 writ\$3 communicat\$3)	EPO; JPO; DERWENT; IBM_TDB	2004/05/18 11:12		0
45	BRS	6	(6722572.pn. 5679007.pn. 20030168511.pn. 20030060085.pn. 20020080142.pn. 6381513.pn. 5561628.pn.) and (parallel simultaneous\$2 concurrent\$2)	USPAT; US-PGPUB	2004/05/18 13:43		0

	Type Hits	Search Text	DBs	Time Stamp	Comments	Errors
46 BRS 236		700/231.ccls. not (361/737.ccls. 710/302.ccls. 710/301.ccls. 710/64.ccls. 710/63.ccls. 710/62.ccls. 235/492.ccls. 235/435.ccls. 235/439.ccls. 235/440.ccls. 235/441.ccls.)	USPAT; US-PGPUB	2004/05/18 13:05		0